

Amendment to the Claims

This listing of claims will replace all prior versions, and listing, of claims in the application:

Listing of Claims:

Claims 1-21 (Cancelled)

22.(Previously Presented) A computer system, comprising:

 a system CPU;

 a liquid crystal display;

 one or more cold cathode fluorescent lamps (CCFL) illuminating said display;

 an inverter circuit generating an AC signal to power said one or more lamps; and

 an inverter controller adapted to generate a plurality of signals to drive said

inverter circuit, said controller comprising at least one input pin configured to receive two or more input signals, each said input signal supporting an associated function of said controller.

23. (Previously Presented) A computer system as claimed in claim 22, wherein said inverter circuit selected from a push-pull, half bridge and full-bridge inverter topologies.

24. (Previously Presented) A computer system as claimed in claim 22, wherein said inverter circuit comprises a plurality of power switches for converting a DC signal to said AC signal.

25. (Previously Presented) A computer system as claimed in claim 22, wherein said inverter circuit comprises a step-up transformer receiving said AC signal and generating a stepped-up AC signal.

26. (Previously Presented) A computer system as claimed in claim 22, wherein said input pin configured to receive a first signal representing a dim voltage, said first signal having a first voltage range; and a second signal representing a voltage feedback signal indicative of voltage supplied to said CCFL, said second signal having a second voltage range.

27. (Previously Presented) A computer system as claimed in claim 22, further comprising a multiplexer circuit to direct one of said input signals to a first circuit to support a first said function of said controller, and to direct another of said input signals to a second circuit to support a second said function of said controller.

28. (Previously Presented) A computer system as claimed in claim 22, wherein one of said input signals is present in a first time period and another of said input signals is present in a second time period.

Claims 29-34 (Cancelled)